

**Notice of References Cited**

Application/Control No.

09/976,927

Applicant(s)/Patent Under

Reexamination

RAMANATH ET AL.

Examiner

Erik Kielin

Art Unit

2813

Page 1 of 1

**U.S. PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-			
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	C	US-			
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**FOREIGN PATENT DOCUMENTS**

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**NON-PATENT DOCUMENTS**

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Wolf, et al. Silicon Processing for the VLSI Era, Vol. 1-Process Technology, 2nd ed., Lattice Press: Sunset Beach CA, 2000, pp. 438, 782-783.
	V	
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